

2N7002T N-Channel MOSFET

General description

N-Channel MOSFET

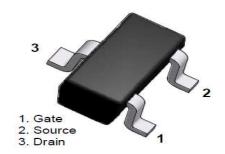
FEATURES

- Low On-resistance
- Low Gate Threshold Voltage
- Low Input capacitance
- RoHS Compliant
- Green EMC
- Matte Tin(Sn) Lead Finish
- Weight: approx. 0.002g

Absolute Maximum Ratings T_A = 25°C unless otherwise noted

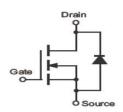
Symbol	Parameter	Value	Units	
V _{DS}	Drain-Source Voltage	60	V	
V_{GS}	Continuous Gate-Source Voltage	±20V	V	
I _D	Continuous Drain Current	115	mA	
P _D	Power Dissipation	150	mW	
Reja	Thermal Resistance from Junction to Ambient	833	°C /W	
Тѕтс	Storage Temperature Range	-55 to +150	°C	
TJ	Operating Junction Temperature	+150	°C	

Green Product



SOT-523

Electrical Symbol:



Device Marking Code:



Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

Off Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
			Min	Тур	Max	
BVpss	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =10uA	60			Volts
lgss	Gate-Body Leakage	V _{DS} =0V, V _{GS} =±20V			±1	uA
IDSS	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V			100	nA

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On Characteristics

Symbol	Parameter	Test Condition	Limits			- Unit
			Min	Тур	Max	
V _{th(GS)}	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D =250uA	1		2.5	Volts
ID(ON)	On-state Drain Current	V _{GS} =10V, V _{DS} =7V	500			mA
RDS(on)	Drain-Source On-Resistance	V _{GS} =10V, I _D =500mA			7.5	Ω
		V _{GS} =5V, I _D =50mA			7.5	Ω
g fs	Forward Trans Conductance	V _{DS} =10V, I _D =200mA	80		500	ms
V _{DS(on)}	Drain-Source On-Voltage	V _{GS} =10V, I _D =500mA			3.75	V
		V _{GS} =5V, I _D =50mA			0.375	V
VsD	Diode Forward Voltage	I _S =250mA, V _{GS} =0V			1	V

Dynamic Characteristics

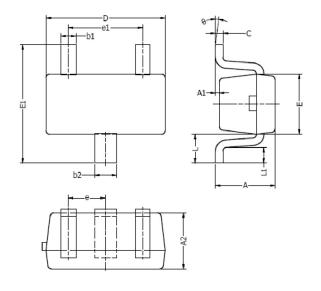
Symbol	Parameter	Test Condition	Limits			Unit
Cymbol			Min	Тур	Max	
Ciss	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz			50	pF
Coss	Output Capacitance				25	pF
Crss	Reverse Transfer Capacitance				5.0	pF

Switching Characteristics

Symbol	Parameter	Test Condition	Limits			Unit
Cymbol			Min	Тур	Max	
t D(on)	Turn-on Time	V_{DD} =10V, R_L =20 Ω ,		5.6		nS
t D(off)	Turn-off Time	I_D =500mA, V_{GEN} =10V, R_G = 10 Ω		25		nS

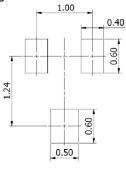


SOT-523 Package Outline



DIM	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
A	0.70	0.90	0.028	0.035	
A1	0.00	0.10	0.000	0.004	
A2	0.70	0.80	0.028	0.031	
b1	0.15	0.25	0.006	0.010	
b2	0.25	0.35	0.010	0.014	
С	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
E1	1.45	1.75	0.057	0.069	
е	0.50 TYP.		0.020 TYP.		
e1	0.90	1.10	0.035	0.043	
L	0.40 REF.		0.016 REF.		
L1	0.10	0.30	0.004	0.012	
θ	0 °	8°	O°	8°	

Typical Soldering Pattern:



Note:

- 1. Above package outline conforms to JEITA EAIJ ED-7500A SC-75A.
- 2. Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



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